

ABSTRACT OF THE DISCLOSURE

5 A processor operable to perform a plurality of functions, the processor
comprising: an input port; a storage element operable to receive and to store an input
signal input via the input port, the input signal comprising at least one control value;
control logic operable to control at least one of the functions of the processor in
dependence on the at least one control value; and access logic operable to receive an
access control signal and to disable access via the input port to the at least one control
10 value stored in the storage element in dependence upon the access control signal.

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